FIG. 2

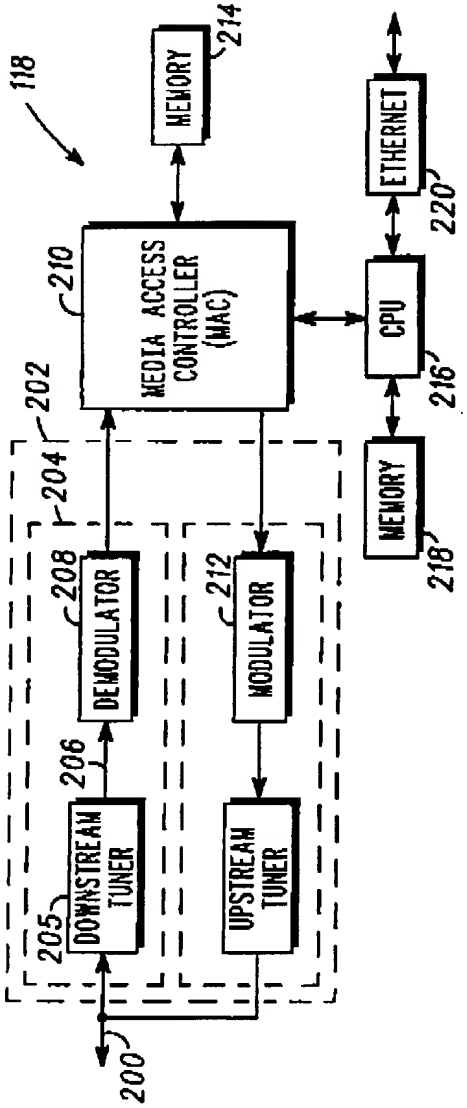
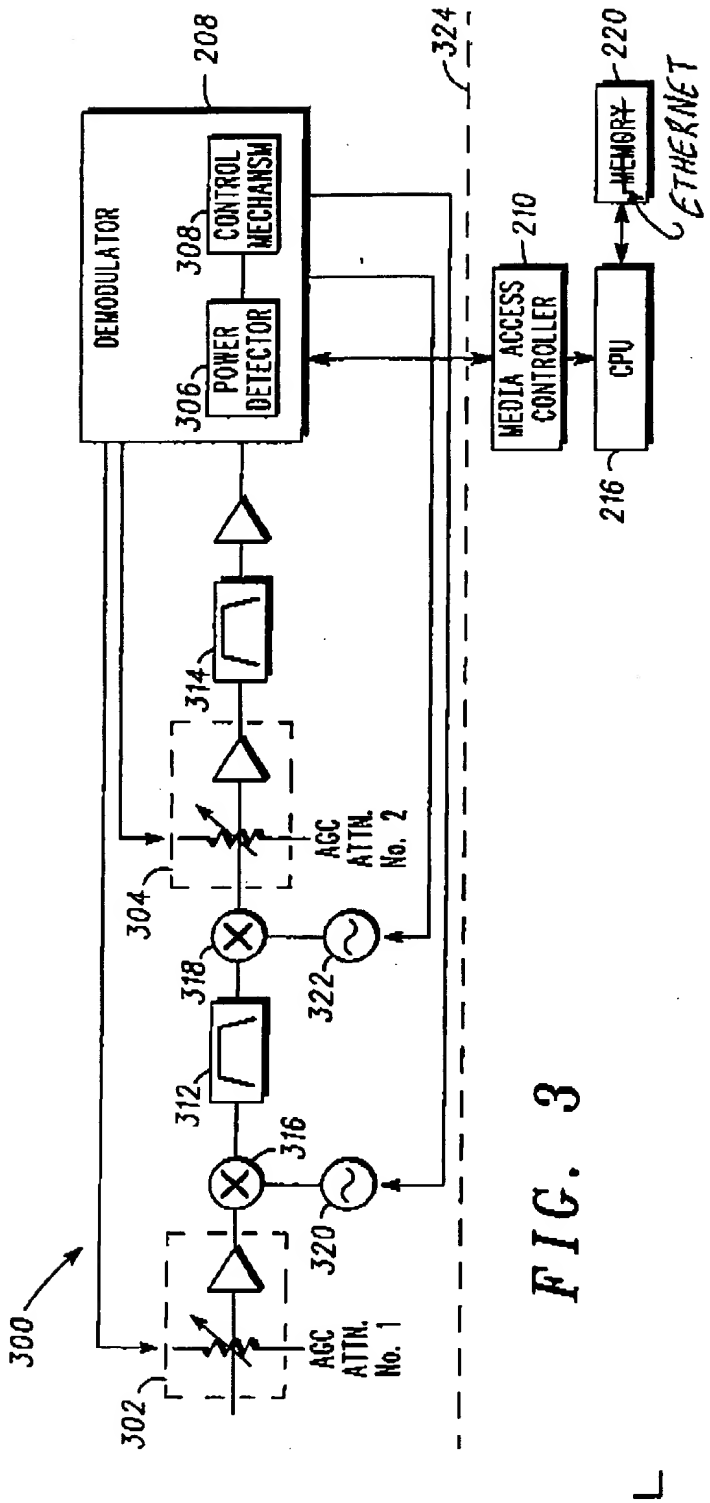

118
MEMORY
214
ETHERNET
220
MEDIA ACCESS CONTROLLER (MAC)
210
CPU
216
202
204
MEMORY
218
208
DEMODULATOR
212
MODULATOR
206
DOWNSTREAM TUNER
205
UPSTREAM TUNER
200

FIG. 3


208
DEMODULATOR
308
CONTROL MECHANISM
306
POWER DETECTOR
324
MEDIA ACCESS CONTROLLER
210
CPU
216
MEMORY
220
ETHERNET
314
AGC ATTN. No. 2
304
318
322
312
316
320
300
AGC ATTN. No. 1
302